

REMARKS

The present Amendment amends claim 1, 6 and 7 and leaves claims 2-5, 8 and 9 unchanged. Therefore, the present application has pending claims 1-9.

Claims 1-3 and 7 stand rejected under 35 USC §102(b) as being anticipated by Applicants' alleged admitted prior art; and claims 4-6, 8 and 9 stand rejected under 35 USC §103(a) as being unpatentable over Applicants' alleged admitted prior art. These rejections are traversed for the following reasons. Applicants submit that the features of the present invention as now more clearly recited in claims 1-9 are not taught or suggested by Applicants' alleged admitted prior art whether taken individually or in combination with any of the other references of record. Therefore, Applicants respectfully request the Examiner to reconsider and withdraw these rejections.

Amendments were made to claim 1 from which claims 2-9 depend to more clearly describe features of the present invention as recited in the claims. Particularly, amendments were made to claim 1 to more clearly recite that the present invention provides a video transmission apparatus for connecting to a network.

According to the present invention the video transmission apparatus, for example, as illustrated in Fig. 3 of the present application includes a central processor unit block 31 and a peripheral block 32.

Further, according to the present invention the peripheral block 32 includes a video processing unit 8 for processing video signals from an image pick-up device 14 and generating video data, a network control unit 7 for controlling transmission and reception of the video data transmitted and

received through a transmission medium D inclusive of a network 40 and a first bus B for providing a series connection, without any branch, of the video processing unit 8 and the network control unit 7.

Still further, according to the present invention the central processing block 31 includes a central processing unit 1 for processing the video data, a storage unit 3 for storing video data from the video processing unit 8, a central control unit 4 for controlling the video processing unit 8, the network control unit 7 and the storage unit 3 in cooperation with the central processing unit 1 and a second bus A for providing a series connection of the central processing unit 1, the storage unit 3 and the central control unit 4.

Still further yet, according to the present invention the first bus B and the second bus A are connected through a first buffer 2.

The above described features of the present invention, particularly with regard to the use of the series connection in the first bus connecting in series the video processing unit and the network control unit and the series connection in the second bus connecting in series the central processing unit, the storage unit and the central control unit, provide unique advantages over that of conventional apparatus. These unique advantages includes reducing the density of the signal wiring of the various buses and reducing the various loads such as stray capacitance occurring in the signal wiring of the various buses. This allows for a driving current for driving the transmission signal to be reduced relative to that of conventional apparatus such as described in the Background of the Invention section of the present application.

The above described features of the present invention now more clearly recited in the claims are not taught or suggested by any of the

references of record whether taken individually or in combination with each other. Particularly, the above described features of the present invention now more clearly recited in the claims are not taught or suggested by Applicants' alleged admitted prior art as discussed in the Background of the Invention section of the present application.

The alleged admitted prior art as discussed in the Background of the Invention section of the present application has various disadvantages enumerated, for example, on page 4, lines 2-16 of the present application. These numerous disadvantages are due to the typical structure of the conventional apparatus having a plurality of dedicated bus systems are provided, wherein the dedicated bus system:

"includes a dedicated bus that enables CPU to simultaneously and independently exchange data with the JPEG compression circuit and with the network control circuit besides a bus system through which CPU gains access to data for an execution command of CPU itself and for data to be processed".

Thus, as is quite clear from the above, the conventional apparatus, such as that taught in the Background of the Invention section of the present application, provides for a plurality of dedicated buses which in effect connect the various elements including the JPEG compression circuit (video processing unit), network control circuit and storage unit in parallel to the CPU, accomplishing the simultaneous and independent operation discussed therein. Therefore, the Background of the Invention section of the present application teaches a conventional apparatus which provides for a parallel connection of the various elements rather than a series connection as now more clearly recited in the claims. The series connection as described above

and as now recited in the claims provides advantages in that it reduces the number of signal lines and reduces unwanted and undesired loads including stray capacitance loads.

Thus, Applicants' alleged admitted prior art fails to teach or suggest that the peripheral block includes a video processing unit for processing video data from an image picking up device and generating video data, a network control unit for controlling transmission and reception of the video data transmitted and received through a transmission medium inclusive of a network, and a first bus providing a series connection, without any branch, of the video processing unit and the network control unit as recited in the claims.

Further, Applicants' alleged admitted prior art fails to teach or suggest that the central processing unit block includes a central processing unit for processing the video data, a storage unit for storing video data from the video processing unit, a central control unit for controlling the video processing unit, the network control unit and the storage unit in cooperation with the central processing unit and a second bus for providing a series connection of the central processing unit, the storage unit and the central control unit as recited in the claims.

In addition to the above described differences between the present invention as recited in the claims and Applicants' alleged admitted prior art, the claims clearly recite that the first and second bus are connected through a first bus buffer. This connection establishes a series connection of the first and second buses which is also not taught or suggested by Applicants' alleged admitted prior art.

Thus, Applicants' alleged admitted prior art even further fails to teach or suggest that the first bus and the second bus are connected through a first buffer as recited in the claims.

Therefore, since Applicants' alleged admitted prior art fails to teach or suggest the features of the present invention as now more clearly recited in the claims, Applicants' alleged admitted prior art does not anticipate nor render obvious the claimed invention. Accordingly, reconsideration and withdrawal of the 35 USC §102(e) rejection of claims 1-3 and 7 as being anticipated by Applicants' alleged admitted prior art and reconsideration and withdrawal of the 35 USC §103(a) rejection of claims 4-6, 8 and 9 as being unpatentable over Applicants' alleged admitted prior art are respectfully requested.


The remaining references of record have been studied. Applicants submit that they do not supply any of the deficiencies noted above with respect to the reference utilized in the rejection of claims 1-9.

In view of the foregoing amendments and remarks, applicants submit that claims 1-9 are in condition for allowance. Accordingly, early allowance of claims 1-9 is respectfully requested.

To the extent necessary, the applicants petition for an extension of time under 37 CFR 1.136. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, or credit any overpayment of fees, to the deposit account of MATTINGLY, STANGER, MALUR & BRUNDIDGE, P.C., Deposit Account No. 50-1417 (500.41299X00).

Respectfully submitted,

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